

PATENT NUMBER

ISSUE CLASSIFICATION	
Class	Subclass

**O.I.P.E.**

AK  
SCANNED 15-3 Q.A. Am

**PATENT DATE**

APPLICATION NO. 09/762751	CONT/PRIOR D F	CLASS <del>350</del>	SUBCLASS 752	ART UNIT 2872	EXAMINER KERVEROS
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Wolf-Dieter Rau

## APPLICANTS

**TITLE**

Method for measuring two-dimensional potential distribution in cmos semiconductor components

PTO-2040  
12/89

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ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner)                      (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)                      (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)                      (Date)		<b>ISSUE BATCH NUMBER</b>	
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